



AVIT
AARUPADAI VEEDU INSTITUTE OF TECHNOLOGY



VINAYAKA MISSION'S
RESEARCH FOUNDATION
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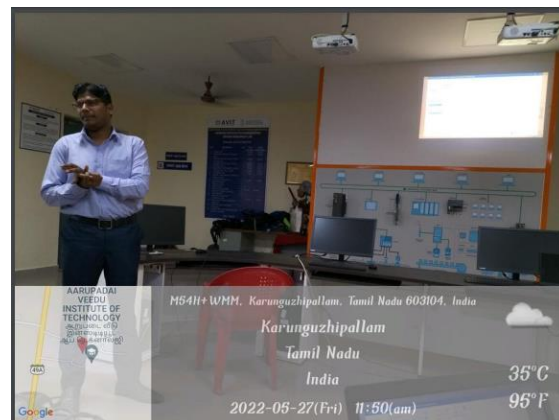


DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Report on Employability Enhancement course Program on “Grid connected and standalone PV solar plant design using PVSYST ”

Three Day Employability Enhancement course was organized by the Department of Electrical and Electronics Engineering on “Grid connected and standalone PV solar plant design using PVSYST” from 26/05/2022 to 28/05/2022.

Day – 1 : 26/05/2022 – Overview of Grid-connected and Standalone using PVsyst.



Event started at 9.30 AM. Dr. L.Chitra, Prof.& Head, EEE has delivered the welcome address and introduced the guest speaker of the day Mr.M.Pradeep, Senior Power System Engineers, Power Project, Chennai. Then, Speaker delivered the lecture on the topic “Basics of Solar Photo Voltaic Technology “. He started the session by giving an overview about opensource software PVSYST and load flow calculation. The session concluded with a interactive session by the speaker with the participants. Vote of thanks was presented after the interaction.

Day – 2: 27/05/2022 – Design of Solar plant and Battery rating for stand-alone

The Day 2 was handled by Mr.P.Kathirmani Selvan,Power System Engineers, Power Project,Chennai. The gathering was welcomed by the Dr.L.Chitra, HOD / EEE.



The session mainly covered following key points

- Albedo effect in PV generation
- Detailed grid-connected project design
- Module selection
- Inverter selection and Sizing calculation
- DC/AC ratio (Pnorm ratio)
- Subarray utilization
- Bifacial module design
- Introduction about HomerPRO

There were around 63 participants in the session. The session concluded with an interactive session by the speaker with the participants. Vote of thanks was presented after the interaction.